

Notice of References Cited	Application/Control No.	Applicant(s)/Patent Under Reexamination	
	10/769,751	HATAKEDA, NORIHITO	
	Examiner	Art Unit	Page 1 of 1
	Almis R. Jankus	2628	

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
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FOREIGN PATENT DOCUMENTS

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	N					
	O					
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	Q					
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	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Peercy et al., "Efficient Bump Mapping Hardware", 1997, ACM.
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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